

FIG.2

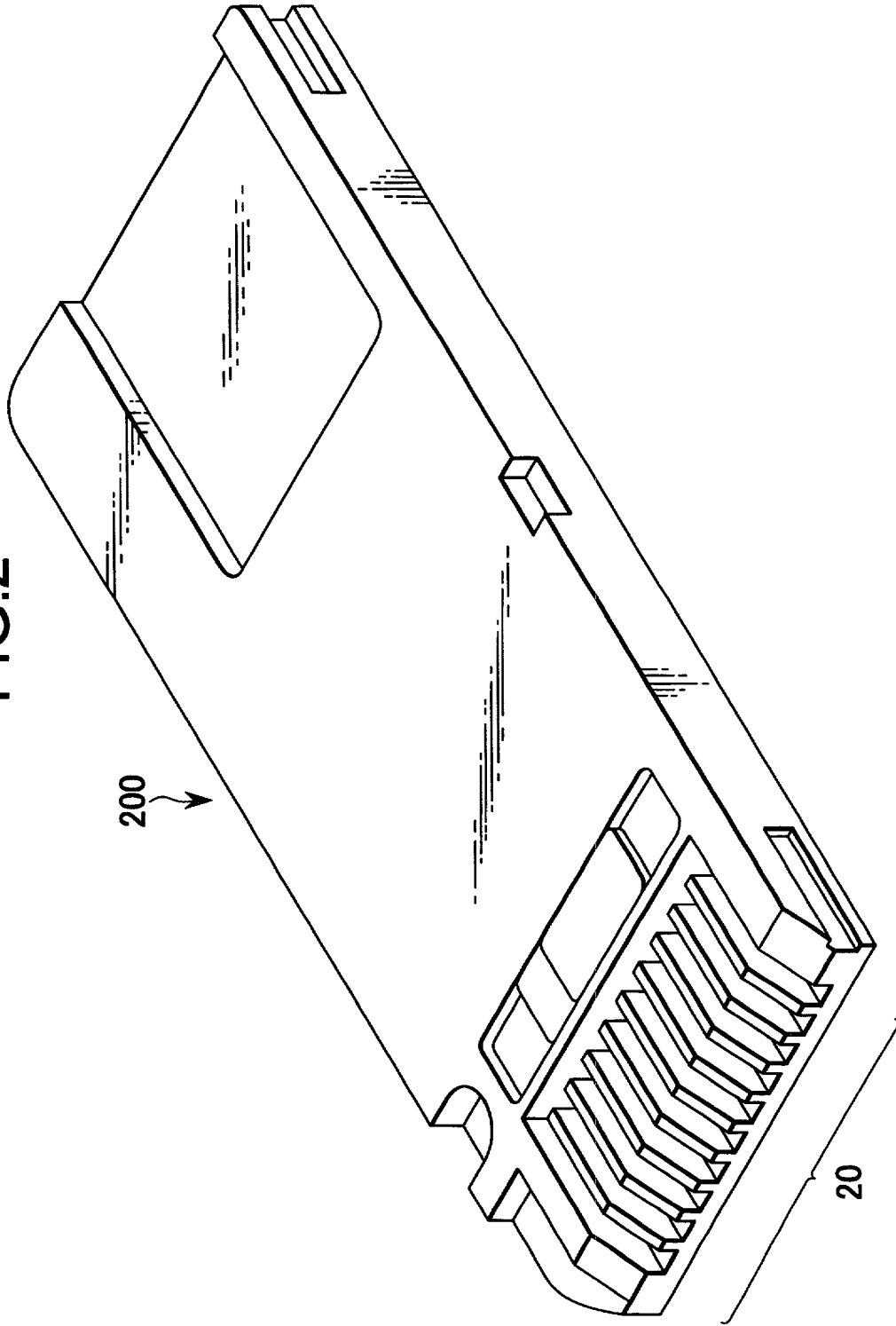


FIG.3

ADDRESS	READ OUT REGISTER	WRITE REGISTER
00		
01	INTERRUPT	
02,03	STATUS	
04	TYPE	TYPE
05		
06	CATEGORY	CATEGORY
07	CLASS	
08	POWER REQUEST	FUNCTION VALIDATION
09,0A	ATTRIBUTE INFORMATION LENGTH	
0B~0F	UNDEFINED	UNDEFINED
10		SYSTEM PARAMETER
11~13		BLOCK ADDRESS
14		COMMAND PARAMETER
15	PAGE ADDRESS	PAGE ADDRESS
16	BLOCK FLAG DATA	BLOCK FLAG DATA
17	BLOCK INFORMATION	BLOCK INFORMATION
18,19	LOGIC ADDRESS	LOGIC ADDRESS
1A~2F	FORMULATION IN PROGRESS	FORMULATION IN PROGRESS

FIG.4

ADDRESS	READ OUT REGISTER	WRITE REGISTER
00		
01	INTERRUPT	
02,03	STATUS	
04	TYPE	TYPE
05		
06	CATEGORY	CATEGORY
07	CLASS	
08	POWER REQUEST	FUNCTION VALIDATION
09,0A	ATTRIBUTE INFORMATION LENGTH	
0B~0F	UNDEFINED	UNDEFINED
10~15	UNABLED	UNABLED
16,17	NOT USED	NOT USED
18	RATE	RATE
19	UART SETTINGS	UART SETTINGS
1A	VOLUME OF REMAINING DATA	RECEPTION DATA LENGTH
1B	FREE BUFFER SPACE	TRANSMISSION DATA LENGTH
1C		RECEPTION INTERRUPT
1D		TRANSMISSION INTERRUPT
1E,1F	UART CLOCK	
20	GPIO INPUT STATUS	GPIO OUTPUT STATUS
21~2F	FORMULATION IN PROGRESS	FORMULATION IN PROGRESS

FIG.5

ADDRESS	READ OUT REGISTER	WRITE REGISTER
00		
01	INTERRUPT	
02,03	STATUS	
04	TYPE	TYPE
05		
06	CATEGORY	CATEGORY
07	CLASS	
08	POWER REQUEST	FUNCTION VALIDATION
09,0A	ATTRIBUTE INFORMATION LENGTH	
0B~0F	UNDEFINED	UNDEFINED
10~15	UNABLED	UNABLED
16,17	NOT USED	NOT USED
18	CAMERA CONTROLLER	CAMERA CONTROLLER
19	RECEPTION DATA LENGTH	TRANSMISSION DATA LENGTH
1A~2F	FORMULATION IN PROGRESS	FORMULATION IN PROGRESS

FIG.6

APPARATUS 100'S SIDEAPPARATUS 200'S SIDE